

FIG. 1A

CPU usage rate			4 Layers
Layer	High	Low	Front-end bus Layer(frequency)
1	100	~ 70	0 (133MHz)
2	80	~ 50	2 (120MHz)
3	60	~ 30	4 (110MHz)
4	40	~ 0	6 (90MHz)

FIG. 1B

Translation Table for CPU front-end bus operation frequency		
SMBW,0	⇒	133MHz
SMBW,1	⇒	125MHz
SMBW,2	⇒	120MHz
SMBW,3	⇒	115MHz
SMBW,4	⇒	110MHz
SMBW,5	⇒	100MHz
SMBW,6	⇒	90MHz

FIG. 1C

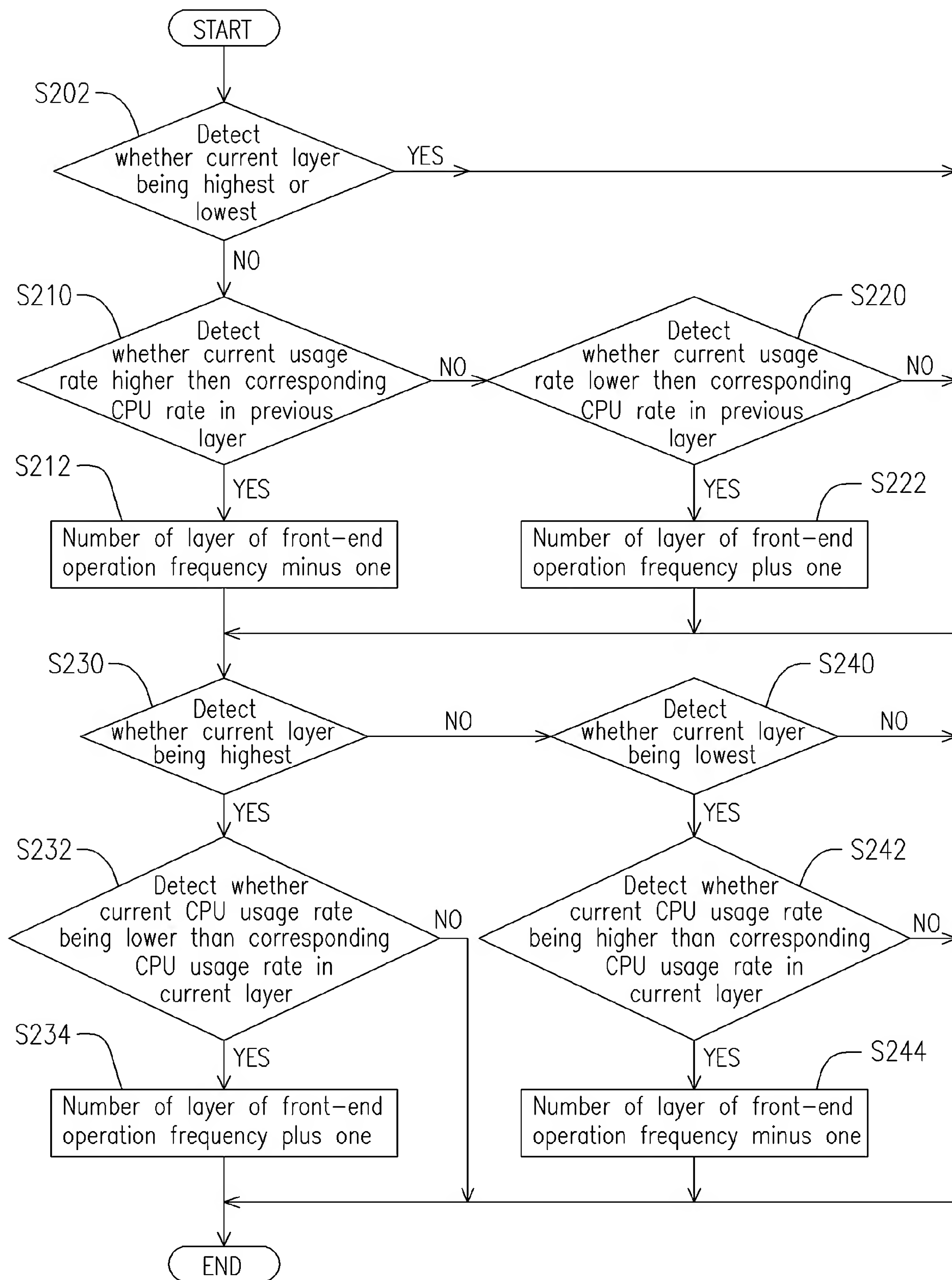


FIG. 2

CPU usage rate			3 Layers
Layer	High	Low	Front-end bus Layer(frequency)
1	100	~ 70	0 (133MHz)
2	80	~ 50	2 (120MHz)
3	60	~ 0	6 (90MHz)

FIG. 3

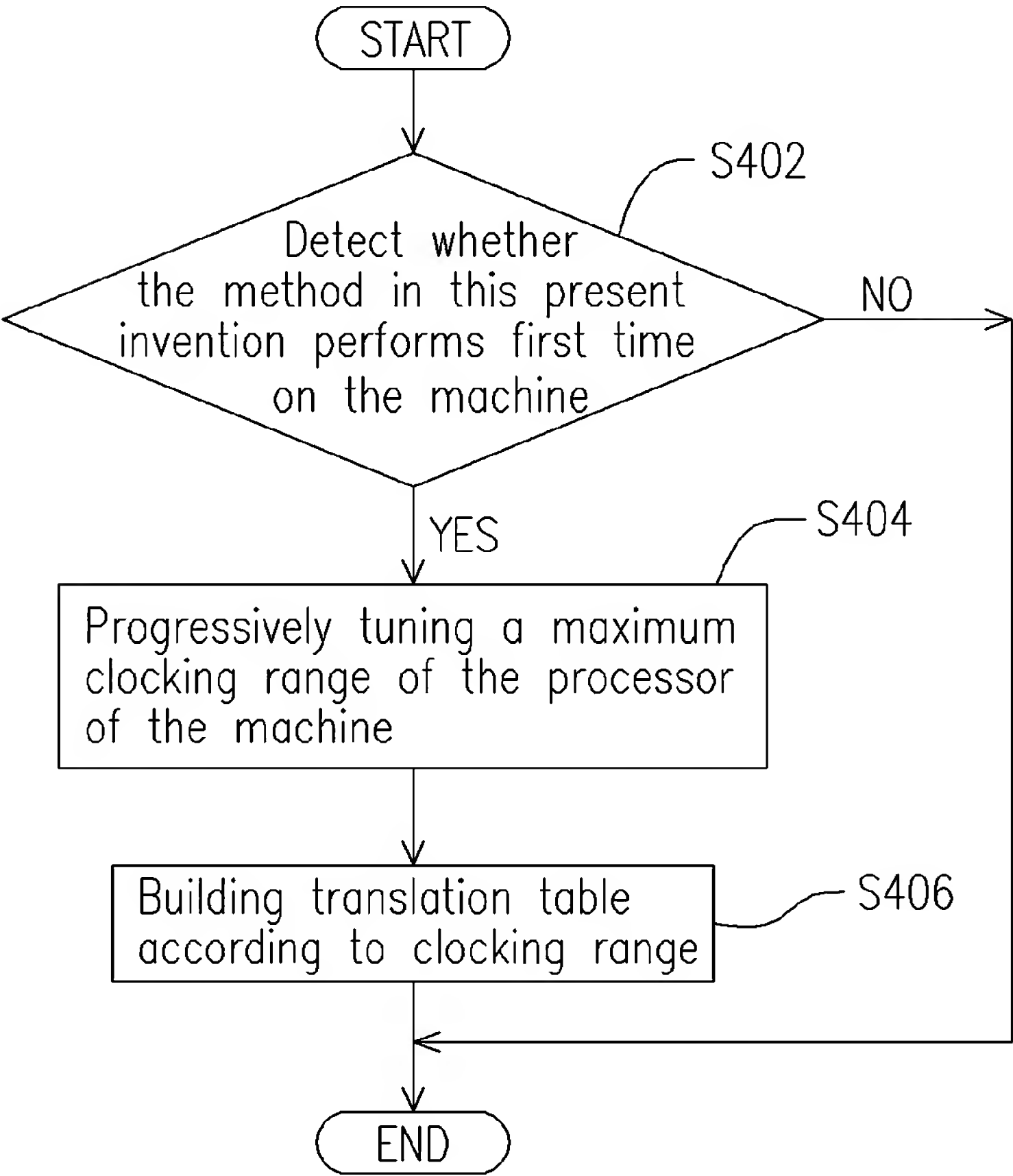


FIG. 4